

Docket No. 210067US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hidemasa ZAMA, et al.

GAU: 2819

SERIAL NO: 09/883,959

EXAMINER: TAN, V.

FILED: June 20, 2001

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLIP FLOP

NOTICE OF APPEAL

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Applicants hereby appeal to the Board of Appeals from the decision dated

The items checked below are appropriate:

- ☐ A Petition for Extension of Time Under 37 C.F.R. §1.136 was filed for \_\_\_\_\_ months.
- ☒ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
- ☒ A Petition for Extension of Time for filing the Notice of Appeal is attached.

Fee: \$320.00

- ☐ Applicant claims small entity status. See 37 CFR 1.27.
- ☒ Is Enclosed
- ☐ Charge to Deposit Account No. 15-0030 (an additional copy of this notice is enclosed herewith).
- ☒ Please charge any additional fees or credit any overpayment of fees required for filing the Notice of Appeal to Deposit Account No. 15-0030. A duplicate copy of this Notice is enclosed.
- ☒ If this notice is not considered timely filed by the U.S. Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this Notice is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

*W. Todd Baker*

\_\_\_\_\_  
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(OSMMN 03/02)

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